

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,909,922 B2
APPLICATION NO. : 09/955007
DATED : June 21, 2005
INVENTOR(S) : Viktor Andrew Tymchenko

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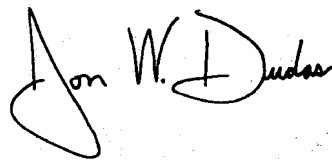
It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Claim 15, Column 9, Line 42 "external input asserted."
should be --external input is asserted. --

Claim 20, Column 10, Line 5 "powerreduction circuit"
should be --power reduction circuit--

Signed and Sealed this

Fourteenth Day of August, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office